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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TC 2800 MAIL ROOM

Application Serial No. 09/420,635
Filing Date October 21, 1999
Inventor Werner Juengling
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner H. Tsai
Attorney's Docket No. MI22-1243
Title: Semiconductor Processing Methods of Forming Devices on a Substrate, Forming
Device Arrays on a Substrate, Forming Conductive Lines on a Substrate, and
Forming Capacitor Arrays on a Substrate, and Integrated Circuitry

RESPONSE TO JANUARY 22, 2001 OFFICE ACTION

To: Box Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.
Tel. 509-624-4276; Fax 509-838-3424
Wells, St. John, Roberts, Gregory & Matkin P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

Responsive to the Office Action dated January 22, 2001, Applicant
amends and remarks as follows:

AMENDMENTS

04/13/2001 FFANAEIA 00000003 09420635

01 FC:102

240.00 DP ✓